

FIG. 1

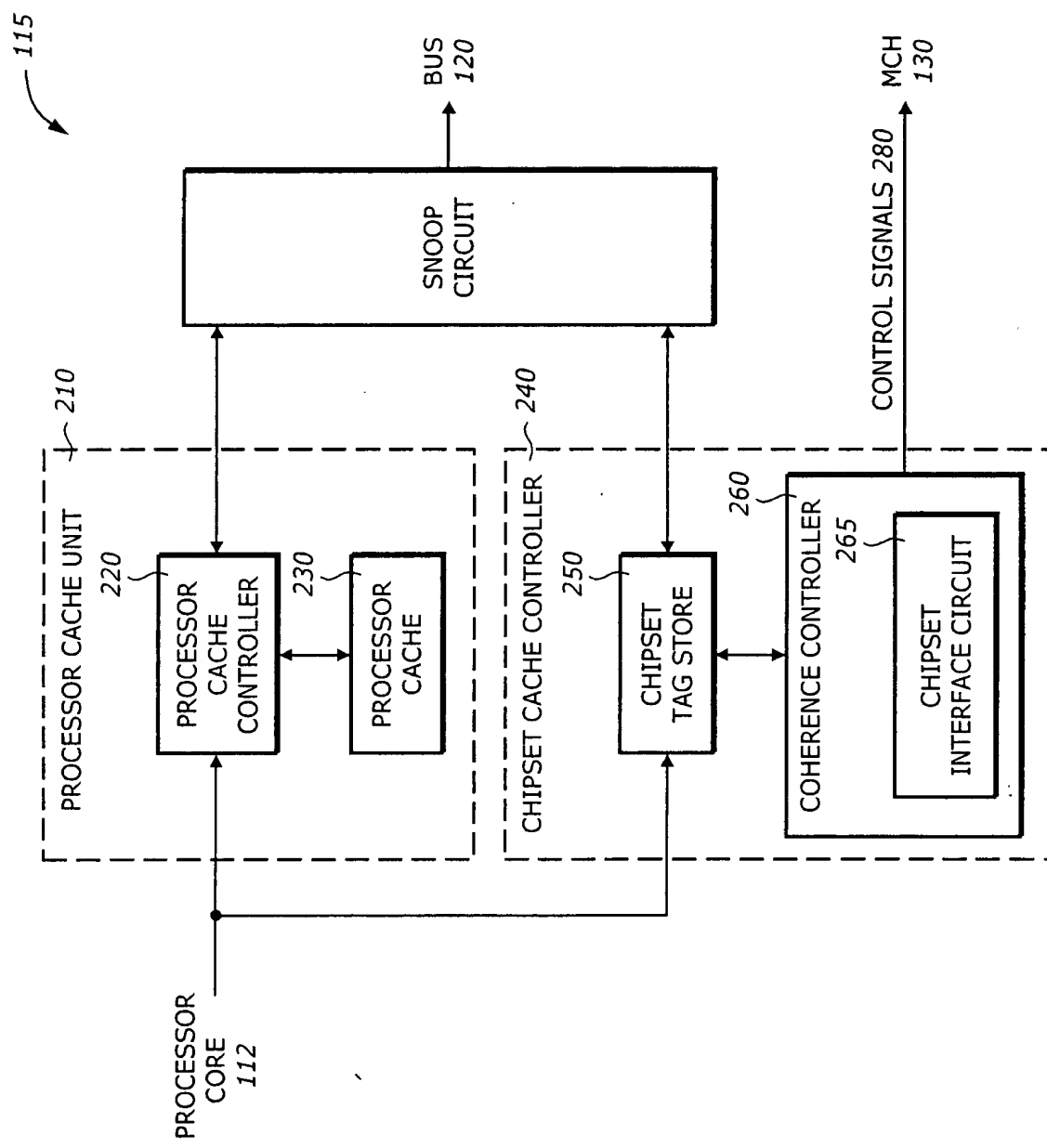


FIG. 2

Figure 3 shows the operation of the cache access type = read. The cache access type = read is performed by the processor. The cache access type = read is performed by the processor.

CACHE ACCESS TYPE = READ				
CACHE VALID INDICATOR	FLUSH INDICATOR	SET IDENTIFIER	OPERATION PERFORMED BY CHIPSET	
NEGATED	NEGATED	SSSS	READ DATA FROM MEMORY INTO CACHE SET SSSS IN CHIPSET. DATA IS SUBSEQUENTLY RETURNED TO PROCESSOR	
NEGATED	ASSERTED	SSSS	READ DATA FROM MEMORY INTO CACHE SET SSSS IN CHIPSET. FLUSH EXISTING DATA IN CACHE SET SSSS TO MEMORY. DATA IS SUBSEQUENTLY RETURNED TO PROCESSOR	
ASSERTED	NEGATED	SSSS	READ DATA FROM CACHE SET SSSS IN CHIPSET AND RETURN DATA TO PROCESSOR	
ASSERTED	ASSERTED	SSSS	NOT VALID	

FIG. 3

CACHE ACCESS TYPE = WRITE

CACHE VALID INDICATOR	FLUSH INDICATOR	SET IDENTIFIER	OPERATION PERFORMED BY CHIPSET
NEGATED	NEGATED	SSSS	WRITE DATA FROM PROCESSOR TO CACHE SET SSSS IN CHIPSET AND IMMEDIATELY TO MEMORY. PUT LINE INTO CLEAN STATE
NEGATED	ASSERTED	SSSS	WRITE DATA FROM PROCESSOR TO CACHE SET SSSS IN CHIPSET. FLUSH EXISTING DATA AT CHIPSET TO MEMORY. WRITE DATA IMMEDIATELY TO MEMORY
ASSERTED	NEGATED	SSSS	WRITE DATA FROM PROCESSOR TO CACHE SET SSSS IN CHIPSET
ASSERTED	ASSERTED	SSSS	WRITE DATA FROM PROCESSOR TO CACHE SET SSSS IN CHIPSET. FLUSH EXISTING DATA TO MEMORY

FIG. 4